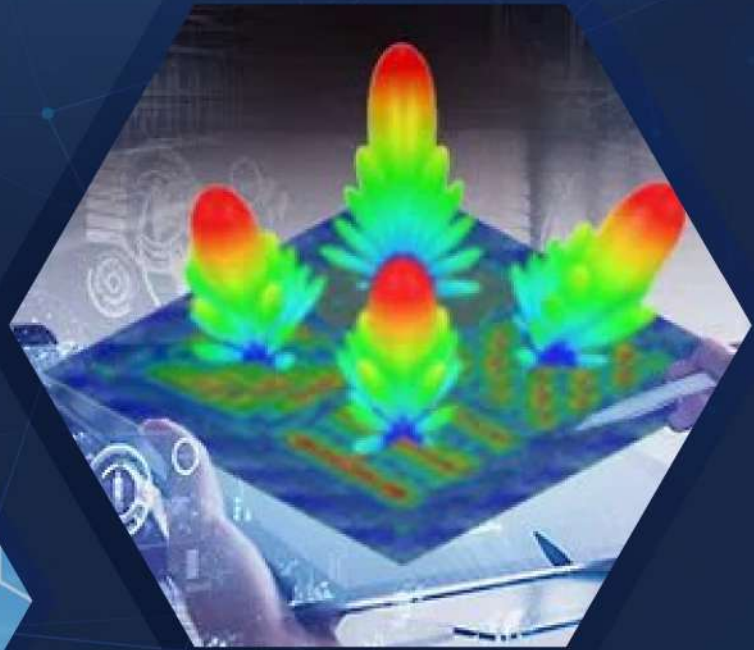




HINDUSTAN

INSTITUTE OF TECHNOLOGY & SCIENCE
(DEEMED TO BE UNIVERSITY)
CHENNAI



WORKSHOP ON DESIGN OF MICROWAVE DEVICES AND 5G ANTENNAS USING ANSYS HFSS SUITE

20, 21 & 25 JUNE 2022

School of Electrical Sciences,
Department of Electronics and Communication Engineering,
Hindustan Institute of Technology and Science,
Padur, Chennai - 603103



ABOUT THE DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

The Department of Electronics and Communication Engineering offers 5G Technology as a specialization along with B.Tech (ECE). Now, the telecom industry is introducing fifth generation (5G) cellular networks and making it the main wireless platform for the human beings, industries, and society. With 5G rising on the horizon, it's an exciting time to be an RF engineer.

Students enrolled in this course, will study electives pertaining to Wireless and 5G Technology Communication starting from the first year. With the help of Texas Instrument Innovation Centre (TIIC) and The German Academy of Digital Education (DADB), students are trained to meet the demands of industries in terms of skilled RF engineers, which escalates our students in various carrier opportunities in Data Patterns, Vestas Technology R & D Chennai Pvt Ltd, Sonus Network, Jisnu Communication, Innospace Automation Services, Arasan Chip Systems, Wafer Space etc. DIY electronic club is the student growth-oriented initiative by the Department of Electronics & Communication Engineering.

With ICT and Alpha Labs, Department of ECE faculties and students collaborate to design and launch a KCG Vergheese Satellite which is a proud initiative hosted by Hindustan Institute of Technology and Science, Chennai. Our Vision is to meet the industrial standards and research in diverse areas of Electronics and Communication Engineering with social commitment. The curriculum is designed to fill the technical gap and needs of the industrial standards. Students will exhibit skills in cutting edge technologies to solve societal needs in multidisciplinary areas.



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ABOUT THE EVENT

Texas Instrument Innovation Centre (TIIC) and DIY Electronic Club, Hindustan Institute of Technology and Science with Entuple Technologies Pvt Ltd, Bangalore is organizing the workshop on "Design of Microwave Devices and 5G antennas Using Ansys HFSS Suite".

This workshop is an effort of qualified microwave domain researchers to help and encourage the research scholars, undergraduate, postgraduate and entrepreneurs to meet their research goals. The workshop concisely delivers the theoretical background of a specific research area, characterization methods, step-wise hands-on for performing the analysis and results generation using Ansys HFSS.

After completing this workshop, the participants will be able to perform the various analysis required to characterize microwave components independently.

Resource Person:

Mr. SHASHIKUMAR.R (PhD-IITJ)

Application Engineer, Entuple Technologies, Bangalore

TARGET AUDIENCE

Students, Research Scholars, Faculty and Industry Experts from various approved Technical Institute and Industry.

Agenda for 20/06/2022, 21/06/2022 and 25/06/2022

Day 1:

Morning: Introduction of tool flow and discuss about different solvers based on wavelength

Evening: Optimization of antenna using ANSYS HFSS

Day 2:

Morning: Pin diode Configuration for polarization switching using ANSYS HFSS

Evening: Metamaterial example using Coupled pair boundary and floquet port configuration using ANSYS HFSS

Day 3:

Morning: Optimization Techniques for antenna applications.

Evening: Modelling and analysis of waveguide using HFSS ANSYS.

Highlights:

- The program is technically supported by ENTUPLE, ANSYS, and Anritsu.
- The program is technically co-sponsored by IEEE- Communication Society.
- Detailed theoretical discussion on research topics and hands-on to generate research outcomes/results.
- The E-certificates will be issued by , Hindustan Institute of Technology and Science, Chennai recognized by ENTUPLE, ANSYS, IEEE- Communication Society.

REGISTRATION FEE:

INR 1,500 for Convenience Charge to be paid through NEFT*

Bank Holder: Hindustan Institute of Technology and Science, Padur, Chennai.

Bank A/c No.: **255402000000001**, IFS Code: **IOBA0002554**

CHIEF PATRON

Dr. Mrs. Elizabeth Verghese
Chancellor

PATRONS

Dr. Anand Jacob Verghese
Pro Chancellor

Mr. Ashok Verghese
Director

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Registrar

Dr. R.W. Alexander Jesudasan
Pro Vice-Chancellor

Dr. Angelina Geetha
Dean E&T

CONVENER

Dr. AL. Vallikannu
Professor & HOD, ECE

COORDINATORS

Ms. K. ThenKumari
Assistant Professor (SG),
ECE

Mr. V. Beslin Geo
Assistant Professor (SG),
ECE

Registration Link:

<https://tinyurl.com/HITSECE5G>



For further queries contact

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