HINDUSTAN
INSTITUTE OF TECHNOLOGY & SCIENCE
(DEEMED TO BE UNIVERSITY)
CHENNAI

Department of ECE

Join us for

A National Level OFFLINE Three-Day FDP

ON

"System Design using PADS and TANNER EDA software tools "

Date

30th June, 1st & 2nd July 2022.

Facilitation

By the Experts of CoreEL Technologies, Bangalore

ABOUT THE INSTITUTION

HINDUSTAN INSTITUTE OF TECHNOLOGY & SCIENCE (HITS) is a leading prestigious and recognized institution in India, located at Chennai, Tamil Nadu. Commenced in 1985, it offers a wide spectrum of Undergraduate, Postgraduate, Diploma, Research & Doctoral Programmes in diverse fields of Engineering, Technology, Architecture, Management, Law, Fashion Design Aviation, Applied Sciences, Allied Sciences, and Arts and Sciences.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1985, with a vision to develop the Department and a Centre of Excellence in Research and Development. Our mission is to develop innovative teaching and learning pedagogies among students and infuse scientific temperament.

The programme provides a sound foundation for students wishing to pursue a career in Electronics Engineering, Communication, Control Systems, Robotics, Sensor Systems, Internet of Things, Artificial Intelligence and VLSI systems through a diverse range of theoretical skills and practical experience which are presented in the context of real time applications.

ABOUT CENSE

We carry out Project Based Learning with the Centre of Sensors and Process control (CENSE). This centre was established to promote focused research on Advanced Sensors, Smart Sensor Systems, Adaptive Robotic Control Systems, Embedded System Design, Bio medical Engineering, Internet of Things, and Intelligent Process Control.

Mark your calendar...
Pen up your ideas...
Brush up your knowledge...

Last date for Registration

28th June, 2022

Payment Details

Online & GPAY to

Account Details

A/C Name
HINDUSTAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Bank Name
IOB Bank, Padur Branch

Account No
255402000000001

IFSC code
IOBA0002554

Registration Fee

Member of IETE Society
Rs. 1,200/-

Faculty / Research Scholars /Students
Rs. 1,500/-

- Certificate will be provided to the Participants.
- FDP will be conducted in OFFLINE mode.

Convener of the program

Dr. AL VALLIKANNU HOD, ECE

Coordinators

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ABOUT THE PROGRAM

PADS is a PCB design package developed by Mentor Graphics. It has powerful layout capabilities to design advanced circuit boards quickly and easily: By using **Tanner EDA** tools we can: design integrated circuits, draw schematics, perform SPICE simulations, do physical design (i.e., chip layout), perform design rule checks (DRC). These tools provide affordable features to help engineers to bring products to market faster.

KEY BENEFITS:

Registered participants

- Will develop their skill in PCB Design with the software: PADS Professional
- Will Learn working with CAD files in PADS software and build their own PCB layout and routing
- Will learn and get to practice with modern circuit electronic analysis techniques through TANNER EDA simulation tools
- Will get the chance to recreate multiple circuit designs with power report and optimization

TOPICS COVERED:

- Concepts on Electronic System Design and Manufacturing
- Schematic Design - using PADS Designer with an example
- Circuit analysis through Simulation
- PDK concepts and Integration with EDA tools
- HDL based ASIC digital design flow
- Analog IC design flow
- , Power report and optimization

WHO CAN ATTEND...

- Faculty/ Students who are interested in learning PCB design and circuit analysis
- Professionals who want to learn PADS & Tanner Professional for building modern PCB circuit
- Electrical & Electronic engineers
- PCB Layout Designers
- Anyone interested in circuit designing and creating printed circuit board
- Researchers

For Registration,

please scan the QR code or Click the link:

https://forms.gle/xaVsGmAA7dtBwFqQ9