

## **FACULTY PROFILE**



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Experience:22 years

Research Area:Vlsi,Image ,Signal,Audio And Video Processing

Recent Publications: 50

1.K.Parthiban, S.Sasikumar (2020), ‘To implement positive feedback adiabatic logic (PFAL)—NAND technique on low power Zigbee applications for processor applications, Microprocessors and Microsystem, Vol.76,102856.

2. Siva Kumar, K., Sasi Kumar, S., & Mohan Kumar, N. (2020). Efficient video compression and improving quality of video in communication for computer encoding applications. *Computer Communications, 153*, 152–158.

<https://doi.org/10.1016/j.comcom.2019.11.026>

**3.AravindBalaji, B., Sasikumar, S. &Ramesh, K. (2020). Automatic SMPS efficiency testing system using labview. *Journal of Advanced Research in Dynamical and Control Systems, 12(4 Special Issue)*, 1355–1363.**

<https://doi.org/10.5373/JARDCS/V12SP4/20201613>