

FACULTY PROFILE



Mr.V.Beslin Geo, M.E, (Ph.D)

Assistant Professor (SG),

Department of ECE,

Hindustan Institute of Technology and Science

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Experience: 9.47 years

Research area: Antenna Analysis and Design, Image Processing.

Recent Publications:

1. Bio-Cell culture processes in Real-Time Monitoring approach with Machine Learning Techniques, International Journal of Biology, Pharmacy and Allied Sciences (IJPAS), Issue 2021, 10 (11), BeslinGeo.V *et al.*, 2021, 10(11).
2. A biasing based current mode analog front-end for biomedical applications, Materials Today: Proceedings, Part of ISSN: 2214-7853 V.Beslin Geo *et al.*, 2021-01.
3. An Analysis and Efficient Approach to Protect CR Networks, Turkish Online Journal of Qualitative Inquiry 12 (3), VB Geo, *et al.*, 2021.
4. Exploiting Localization Capability and Packet Transfer at Intermediate Nodes for Underwater Sensor Networks using Simulations, 2020 Third International Conference on Smart Systems and Inventive Technology (ICSSIT), IEEE, V Beslin Geo, *et al.*, 2020.
5. Wastage Cost Analysis of Fillet Welding on Boiler, Volume 12 | 01-Special Issue, Pages: 505-51, Journal of Advanced Research in Dynamical and Control Systems (JARDCS), V. Beslin Geo, *et al.*, 2020.
6. A Statistical Lower UWB Channel Model for in Body Communications, Mobile Networks and Applications, Springer Link, Volume 24, pages 1814–1820, V. Beslin Geo, *et al.*, 2019.
7. Sumrate Maximization by Optimal Precoder Design for MIMO Retransmission System. Journal of Engineering and Applied Sciences, 14: 3611-3614, V. Beslin Geo, *et al.*, 2019.

8. Journal of Electrical Systems and Information Technology 2018, in the topic of “Gain enhancement of patch antenna integrated with metamaterial inspired superstrate”, V. Beslin Geo, *et al.*, 2018.
9. Journal of Engineering & Technology (UAE) 2018, in the topic of “Extraction of Hidden Text from Images using DWT”, V. Beslin Geo, *et al.*, 2019.
10. Identification Based Code Reform of Object-Oriented Systems, International Journal of Pure and Applied Mathematics, Volume 119 No. 17, 1905-1909, V. Beslin Geo, *et al.*, 2018.

Professional Body Membership

IETE (Institution of Electronics and Telecommunication Engineers)– Life Long Membership

Industry Institution Interaction

Initiated the MoU sign of Verification Solution India Pvt Ltd a Subsidiary company of Tessolve Semiconductor Pvt Limited, Bangalore .

Employment and Skill Patents published/granted

1. Employment and Skill Gap Identification Technique using Machine Learning for ITES Sector / 31-01-2021
2. Securing Critical Digital Assets of Chemical Industries Using Secure Development Operational Environment (SDOE) Framework / 13-05-2021

Competency - HFSS (High Frequency Electronic Simulation) in Antenna Domain.