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**Experience:** 23 years

**Research area:** Wireless Adhoc Networks/Image Processing/VLSI

**Recent Publications:**

- ‘Minimizing the energy consumption of WSN by using modified hybrid energy efficient distributed clustering protocol, in the International Journal of Engineering & Technology, 7 (3.24) (2018) 758-763 with Vallikannu R, Sai Advait Narayanan
- ‘Design of “32” Point Split Radix based Multipath Delay Commutator FFT Architecture for Low Power Applications, in the Indonesian Journal of Electrical Engineering and Computer Science, ISSN: 2502-4752, Vol. 11, No. 3, September 2018, pp. 1042~1047 with A. Manimaran
- ‘Performance investigation of low power radio duty cycling MAC for resource constrained WSN’, in the International Journal of Engineering & Technology, 7 (1.9) (2018) 93-98 with B. Rajesh Shyamala Devi
- ‘Design and Implementation of an Efficient Fft Processor using Modified Booth Multiplier, in the International Journal of Control Theory and Applications, ISSN : 0974-5572, Vol. 10, 2017, pp. 1~9 with A. Manimaran and S.K Sudheer.